



Spirent dX3 2-Port Quad SPEED 400/200/100/50GbE

Native QSFP-DD Test Module
Native CFP8 Test Module
Native OSFP Test Module

Features

- 2 400GbE ports per dX3 module, delivers the highest density high speed Ethernet solution per module, chassis or rack unit
- Each module supports the following port density 2x400GbE, 2x200GbE, 4x100GbE or 8x50GbE
- Support for optical fiber, active optical cables and direct access copper
- Support for Ethernet (FEC), (AN) and (LT)
- Protocol testing for L2/3 routing/switching and data center test cases

Benefits

- Industry's highest density single slot test modules: 4 times CFP8 advantage, 2 times QSFP-DD advantage
- Industry's first native OSFP test module
- Conduct performance, stress, and industry standard benchmark tests
- Provides large capacity testing for a variety of services

Network bandwidth needs continue to grow at a rapid pace. Network equipment manufacturers are developing highly flexible multi-rate products to support the latest generation of HSE devices. Service Providers and Hyperscale data centers are deploying multi-rate networking infrastructure solutions to meet this growing market.

With these multi-rate requirements, customers demand higher density test equipment. Flexibility is needed to validate the next-generation of routers and data center fabrics.

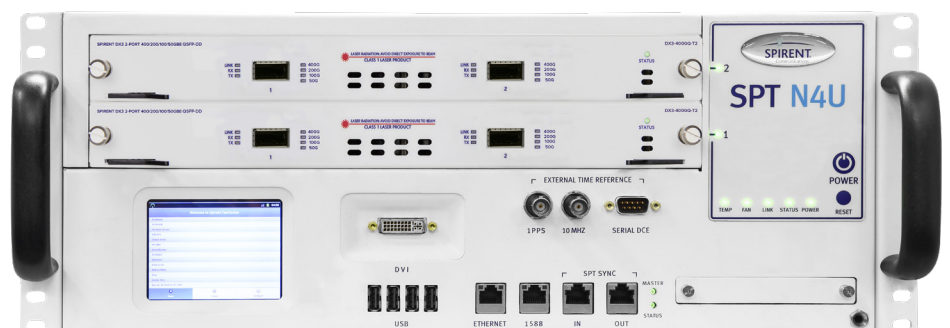
The Spirent dX3 Quad speed module architecture was developed to meet these specific needs with its industry-leading 4 times density advantage for native CFP8 and 2 times density advantage for QSFP-DD and industry first OSFP test module. Spirent's native QSFP-DD, OSFP and native CFP8 module versions can be configured to support up to four speeds per port: 400/200/100/50GbE.

Applications

Cloud Computing/Streaming Services—Validate data plane QoS on thousands of flows at line rate and test complex routing, data center and access protocols on switches and routers. A single N11U can support 24-ports or 4-ports from a single N4U chassis.

Data Center ToR and EoR Switches and Fabrics—Validate forwarding performance, latency, MAC capacity and functional capabilities of ultra-high-scale, next-generation enabled multi-terabit cloud data center fabrics.

Terabit Routers—Test 400 GbE core routers with high-scale, multiprotocol topologies.



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Productivity

- Intelligent Results™
- When creating test beds at the scale needed the amount of data that is produced is astronomical. An advanced, highly efficient distributed database processes billions of real-time results to validate tests and identify problems, giving engineers the immediate feedback they need to debug problems and accelerate development
- Delivers more results with tight correlation, and more information to find those obscure bugs. With more coverage and more information, Spirent answers questions faster, and in a single test run, where multiple runs are necessary with other test tools
- Interesting streams uses real-time results data mining to dynamically filter through mountains of data and display the results that matter
- Powerful automation with Command Sequencer (Visual Programming) and GUI to Script empowers the test operator to:
 - Construct sophisticated, stressful, automated test cases without programming experience
 - Combine numerous individual test cases into a single run to save regression test time
 - Develop a catalog of broad automated test cases in a fraction of the time
 - Export automated test cases to run from a command line for headless test execution that can be integrated with any automated regression system

Extensive, Flexible Reporting—Real-time statistics for critical variables across all protocols. Using Spirent's iTest platform, your device under test results can easily be correlated and compared with Spirent's results.

Technical Specifications

Spirent dX3 Module

Module Part Number	Speed	Maximum Ports per slot	Maximum Ports per SPT-N11U Chassis	Maximum Ports per SPT-N4U Chassis
DX3-400GQ-T2	400/200/100/50GbE	2/2/4/8	24/24/48/96	4/4/8/16
DX3-400GO-T2	400GbE Only	2	24	4
DX3-200GO-T2	200GbE Only	2	24	4
DX3-400GD-T2	400/100GbE	2/4	24/48	4/8
DX3-400GDF-T2	400/50GbE	2/8	24/96	4/16
DX3-400GQ-P2	400/200/100/50GbE	2/2/4/8	24/24/48/96	4/4/8/16
DX3-400GO-P2	400GbE Only	2	24	4
DX3-200GO-P2	200GbE Only	2	24	4
DX3-400GD-P2	400/100GbE	2/4	24/48	4/8
DX3-400GDF-P2	400/50GbE	2/8	24/96	4/16
DX3-400GQ-O2	400/200/100/50GbE	2/2/4/8	24/24/48/96	4/4/8/16
DX3-400GO-O2	400GbE Only	2	24	4
MSA Interface	QSFP-DD, OSFP or CFP8			
Operational modes	400, 200, 100, 50GbE			
Port CPU	Stackable multi-core CPU			
User reservation	Per port			
Test Port speed config	2 test port speed groups per blade			
Line clocking and packet time-stamping	Stratum-3 rated oscillator is the default time source. Transmit line clock is at the precise nominal Ethernet rate $\pm < 1$ PPM on initial shipment. Accurate to ± 4.6 PPM 15 years of operation <ul style="list-style-type: none">▪ Frame time-stamp resolution of 2.5ns▪ GPS and CDMA-based external time sources are supported▪ IEEE 1588v2 and NTP packet-based external time sources are supported▪ TIA/EIA-95B-based external time sources are supported			
Inter-module and inter-chassis time synchronization	Modules in the same chassis are phased-locked to the timing source of the control module. For more modules in separate chassis: <ul style="list-style-type: none">▪ Spirent-patented self-calibrating inter-chassis timing chain using dedicated port on chassis control module delivers precise synchronization ± 20ns▪ Synchronization via external GPS or CDMA network▪ Using IEEE 1588 or NTP packet-based approaches▪ With TIS/EIA-95B timing inputs			

Spirent dX3 Module (continued)

Module weight	3.219 kg, 5.45lbs.
Module predicted MTBF	56,330 hours. Hours of continuous operation
Operating temperature range	Supported for 41° to 95° F (5° to 35° C) ambient temperature. 20% to 80% relative humidity
Max power draw per module	Maximum of 450W per slot

Spirent TestCenter Layer 2-3 Generator and Analyzer

Number of streams	<ul style="list-style-type: none"> ■ Stats/Streams @400G; Tx=4K Rx=4K/1K (Basic Stats/ Latency stats) ■ Stats/Streams @200G; Tx=4K Rx=4K/1K (Basic Stats/ Latency stats) ■ Stats/Streams @100G; Tx=1K Rx=1K/256 (Basic Stats/ Latency stats) ■ Stats/Streams @50G; Tx=512 Rx=512/128 (Basic Stats/ Latency stats) ■ Stream fields can be varied to create billions of flows
Frame transmit modes	Port based (rate per port), stream based (rate per stream), burst, timed
Min/max frame size (w/CRC)	60 to 16,004
Min/max Tx rates	1 packet per 3.43 seconds to 101% of line rate
Real-time Tx stream adjustments	Change rate and frame length settings without stopping the generator or analyzer for truly interactive, cause and effect analysis
Per-stream statistics analyzed in real time	<p>Tx and Rx frame counts and rates</p> <ul style="list-style-type: none"> ■ Tx and Rx Layer 1 byte counts and rates ■ Out of sequence errors ■ FCS errors and rate ■ Min, Max and Average Latency (4K streams) ■ Real Time Dropped Frame count
Per-port statistics analyzed in real time	<p>Tx and Rx frame counts and rates</p> <ul style="list-style-type: none"> ■ Tx and Rx Layer 1 byte counts and rates ■ Out of sequence errors ■ PRBS errors ■ FCS errors and rate
Transmit timestamp resolution	2.5 ns Tx timestamp resolution with intra-chassis and inter-chassis synchronization
Supported encapsulations	<ul style="list-style-type: none"> ■ Layer 2: Ethernet II, 802.1Q, 802.1ad, FCoE ■ Layer 3/4: IPv4, IPv6, TDP, UDP
Supported Tx signature capability	Fully compatible with Spirent hardware; contains sequence number and highly accurate timestamp
Capture buffer size	8 MB per port
Capture buffer controls— Spirent TestCenter’s unique capture capability allows maximum effectiveness when debugging hard to find hardware or protocol problems	Several modes of operation that include: Filter by protocol fields, filter by byte offset and range; store slices or full-frames; store signature or all frames; store tx/rx control plane with data plane; real-time mode for control plane traffic; wrap or stop buffer at end. User defined pattern definitions can logically combine 8 filters of up to 32 total bytes. Patterns can be applied to start, filter (quality) or stop capture. In addition to user-patterns, filtering, starting and stopping capture contains the following pre-defined events: FCS, PRBS, IPv4 checksum, TCP/UDP/IGMP checksum, and sequence errors; undersize, oversize, jumbo, and user-defined frame length; IPv4, IPv6, TCP, UDP and IGMP packets; test signature present and test stream ID match. Each event can be independently set to ignore, include or exclude.
Latency modes	Benchmark tests support LIFO, LILO, FIFO or FILO latency calculation methods
Route Insertion Table (RIT) Entries per port	8K 4-byte entries for dynamic label or random IP/MAC address assignments
RIT or List VFD Entries per Stream	8 RIT insertions per stream and 4 VFD insertions per stream

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Native CFP8 Test Module
Native OSFP Test Module



Requirements:

- Spirent chassis and controller (see table)
- Windows-based workstation with 10/100/1000 Mbps Ethernet NIC; mouse and color monitor required for GUI operation
- Linux or Windows-based workstation for scripting
- Mac, Linux or Windows based workstation for Rest API support

Spirent Services

Spirent Global Services provides a variety of professional services, support services and education services—all focused on helping customers meet their complex testing and service assurance requirements. For more information, visit the Global Services website at www.spirent.com or contact your Spirent sales representative.

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Technical Specifications (cont'd)

Layer 1 Functionality

QSFP-DD, OSFP or CFP8 Interconnects	SR, LR, FR, DR, PSM4 at multi-rate (400/200/100/50GBE)
Media support and FEC options	Support varies by module speed mode <ul style="list-style-type: none">■ 400G: 400GBASE-SR16, 400GBASE-DR4, 400GBASE-LR8, 400GBASE-FR8 plus additional MSA PMDs■ 200G: 200GBASE-SR4, 200GBASE-PSM4, 200GBASE-LR/FR4, plus additional MSA PMDs■ 100G: 100GBASE-SR2, 100GBASE-LR2 plus additional MSA PMDs■ Clause 119 RS-FEC, Clause 91 RS-FEC (100GBASE-SR4 and DR)■ Direct Access Copper breakouts
AN/LT (Enable/Disable)	CR tbd on standard
Layer-1 Debug Tools & Features	CR Tx Emphasis settings, Rx Eye view, FEC Counters, PRBS Gen/Check, Front-end L1 Summary Status, Xcvr MDIO access

Layer 4-7 Application and Security

IP Version Supported	IPv4 and IPv6
Encapsulation Protocols	802.1Q and 802.1 Q-in-Q
Transport Protocols	TCP, UDP
Data Protocols	HTTP, SIP and FTP, Unicast/Multicast RTSP and RAW TCP
Authentication Protocols	802.1x
Network Access Protocol	DHCP and PPPoE
Network Realism Fragmentation	Line speed limitation, network latency, packet loss and fragmentation
Video Protocols	RTSP/RTP, Multicast streaming, IGMPv2, IGMPv3 and MLDv2
Video Codecs	H.263 and H.264
Video Quality Measurement	MDI measurements along with additional statistics to detect picture quality
Voice Codecs	G711A, G711U, G.723.1, G.726-32, G.728 and G.729AB
Voice Protocols	SIP over UDP

Ordering Information

Part Number	Description	Spirent TestCenter
Test Modules		
DX3-400GO-T2	SPIRENT DX3 400GBE ONLY QSFP-DD 2-PORT	X
DX3-200GO-T2	SPIRENT DX3 200GBE ONLY QSFP-DD 2-PORT	X
DX3-400GQ-T2	SPIRENT DX3 400G/200/100/50GBE QSFP-DD 2-PORT	X
DX3-400GD-T2	SPIRENT DX3 400/100GBE QSFP-DD 2-PORT	X
DX3-400GDF-T2	SPIRENT DX3 400/50GBE QSFP-DD 2-PORT	X
DX3-400GO-P2	SPIRENT DX3 400GBE ONLY CFP8 2-PORT	X
DX3-200GO-P2	SPIRENT DX3 200GBE ONLY CFP8 2-PORT	X
DX3-400GQ-P2	SPIRENT DX3 400G/200/100/50GBE CFP8 2-PORT	X
DX3-400GD-P2	SPIRENT DX3 400/100GBE CFP8 2-PORT	X
DX3-400GDF-P2	SPIRENT DX3 400/50GBE CFP8 2-PORT	X
DX3-400GO-O2	SPIRENT DX3 400GBE ONLY OSFP 2-PORT	X
DX3-400GQ-O2	SPIRENT DX3 400G/200/100/50BE OSFP 2-PORT	X

Additional Features

UPG-ANLT-T2	UPGRADE QSFP-DD-T2 AN/LT
UPG-ANLT-P2	UPGRADE CFP8-P2 AN/LT

Spirent Chassis

SPT-N11U-110	Spirent N11U chassis and controller with 110VAC power supplies
SPT-N11U-220	Spirent N11U chassis and controller with 220VAC power supplies
SPT-N4U-110	Spirent N4U chassis and controller with 110VAC power supplies
SPT-N4U-220	Spirent N4U chassis and controller with 220VAC power supplies